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		ollection of information unless it displays a valid OMB control number. 10/622,627 (patent # 6,879,195)		
TRANSMITTAL	Filing Date	07/17/2003 (issued date 04/12/05)		
FORM	First Named Inventor	Michael Green		
i Oiliu	Art Unit	2816		
(to be used for all correspondence after initial filing)	Examiner Name	Linh M. Nguyen		
Total Number of Pages in This Submission 2	Attorney Docket Number	RA-307 (RA307.P.US)		
ENCLOSURES (Check all that apply)				
Fee Transmittal Form	Drawing(s)	After Allowance Communication to TC		
Fee Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences		
Document(s)  Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53	Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence A Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on CD	Address  Status Letter Other Enclosure(s) (please Identify below):  1. PTO/SB/44 "Certificate of Correction" 2. Return Receipt Postcard  Certificate JUN 2 4 2005  of Correction		
	OF APPLICANT, ATTOR	RNEY, OR AGENT		
Firm Name Silicon Edge Law Group LLP, Art	fruy J. Behiel Patent Attorney			
Signature				
Printed name Arthur J. Behiel		-		
Date June 17, 2005	R	Reg. No. 39,603		
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO.

: 6,879,195

APPLICATION NO.: 10/622,627

ISSUE DATE

: 04/12/2005

INVENTOR(S)

Michael Green, Nhat M. Nguyen, Yohan Frans, Dennis Kim, and Todd Bystrom.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 20, Line Number 34

A circuit comprising:

a phase-locked loop circuit that receives a reference clock signal and generates a feedback clock signal; and means for detecting when the feedback clock signal and the reference clock signal are out of lock, wherein the means detects that the feedback clock signal and the reference clock signal are out of lock when a like "-- -- number of -- -- " feedback clock cycles and reference clock cycles occur during a time period and when a single rising edge and a single falling edge of the feedback clock signal are detected during less than a predetermined number of the reference clock cycles during the time period.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Silicon Edge Law Group LLP, Arthur J. Behiel, Patent Attorney 6601 Koll Center Parkway, Suite 245 Pleasanton, California 94566

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